



RECEIVED

PATENT APPLICATION

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233

9/a
B. TURNER

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

FEB 14 1989

*note: new
drawings
acknowledged
prior art
paper*
In re application of:

TAKAHIKO URAI

Serial No.: 07/103,312

Group Art Unit: 233

Filed: October 1, 1987

Examiner: Popek

For: SEMICONDUCTOR MEMORY DEVICE WITH IMPROVED CELL ARRANGEMENT

AMENDMENT

3/7/3/89
Honorable Commissioner of
Patents and Trademarks
Washington, D.C. 20231

Sir:

In response to the outstanding Office Action dated August 8, 1988, please amend the above-identified application as follows.

IN THE CLAIMS:

1
Please cancel claims, 1, 2, 3 and 5 without prejudice or disclaimer.

A
1. (Amended) A semiconductor memory device comprising means receiving address information consisting of N bits (N being a positive integer), an array of memory cells arranged in a matrix form of rows and columns, each of said memory cells having the same structure, the number of said memory cells in said array being larger than [the] 2^N , said memory cells in said array being divided into a peripheral group and an internal group, the memory cells of said internal group being completely surrounded by the